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## In the specification:

Please replace the paragraph 3, starting on page 1, through and including paragraph 12, on page 2, of the patent application with the following paragraphs:

U.S. patent application serial number 10/045,792 by T.B. Berg et al., new U.S. Part. No. 6, 807, 526 (BEA919990003US1) entitled "Method And Apparatus For Increasing Requestor Throughput By Using Data Available Withholding" was filed on January 9, 2002.

U.S. patent application serial number 10/045,927 by T.B. Berg et al. 3till pending, (BEA920000017US1) entitled "Method And Apparatus For Using Global Snooping To Provide Cache Coherence To Distributed Computer Nodes In A Single Coherent System" was filed on January 9, 2002.

U.S. patent application serial number 10/045,821 by T.B. Berg et al., now U.S. Pat. No. 6, 725, 779, (BEA920000018US1) entitled "Multi-level Classification Method For Transaction Address Conflicts For Ensuring Efficient Ordering In A Two-level Snoopy Cache Architecture" was filed on January 9, 2002.

U.S. patent application serial number 10/045,564 by S.G. Lloyd et al. 3/ill pending, (BEA920000019US1) entitled "Transaction Redirection Mechanism For Handling Late Specification Changes And Design Errors" was filed on January 9, 2002.

U.S. patent application serial number 10/045,797 by T.B. Berg et al., now U.S. Pat. No. 6, 795, 889, (BEA920000020US1) entitled "Method And Apparatus For Multi-path Data Storage And Retrieval" was filed on January 9, 2002.

U.S. patent application serial number 10/045,923 by W.A. Downer et al., Will pending, (BEA920000021US1) entitled "Hardware Support For Partitioning A Multiprocessor System To Allow Distinct Operating Systems" was filed on January 9, 2002.

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U.S. patent application serial number 10/045.925 by T.B. Berg et al., Still pending, (BEA920000022US1) entitled "Distributed Allocation Of System Hardware Resources For Multiprocessor Systems" was filed on January 2, 2002.

U.S. patent application serial number 10/045,926 by W.A. Downer et al., now U.S. Pat. No. C, 823, 498, (BEA920010030US1) entitled "Masterless Building Block Binding to Partitions" was filed on January 9, 2002.

U.S. patent application serial number 10/045,796 by W.A. Downer et al., 3411 perding, (BEA920010041US1) entitled "Masterless Building Block Binding To Partitions Using Identifiers And Indicators" was filed on January 9, 2002.